

FIG.1

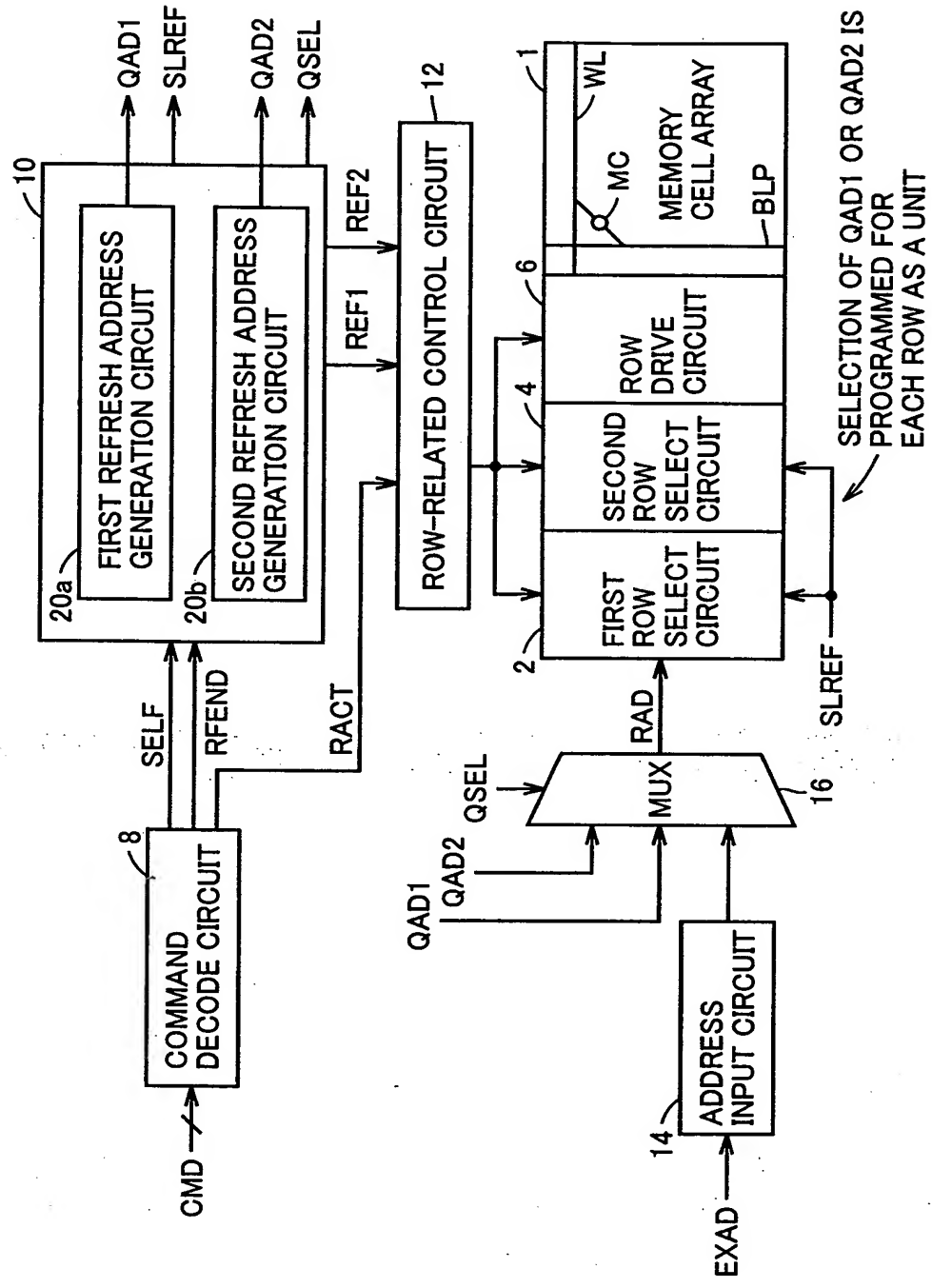
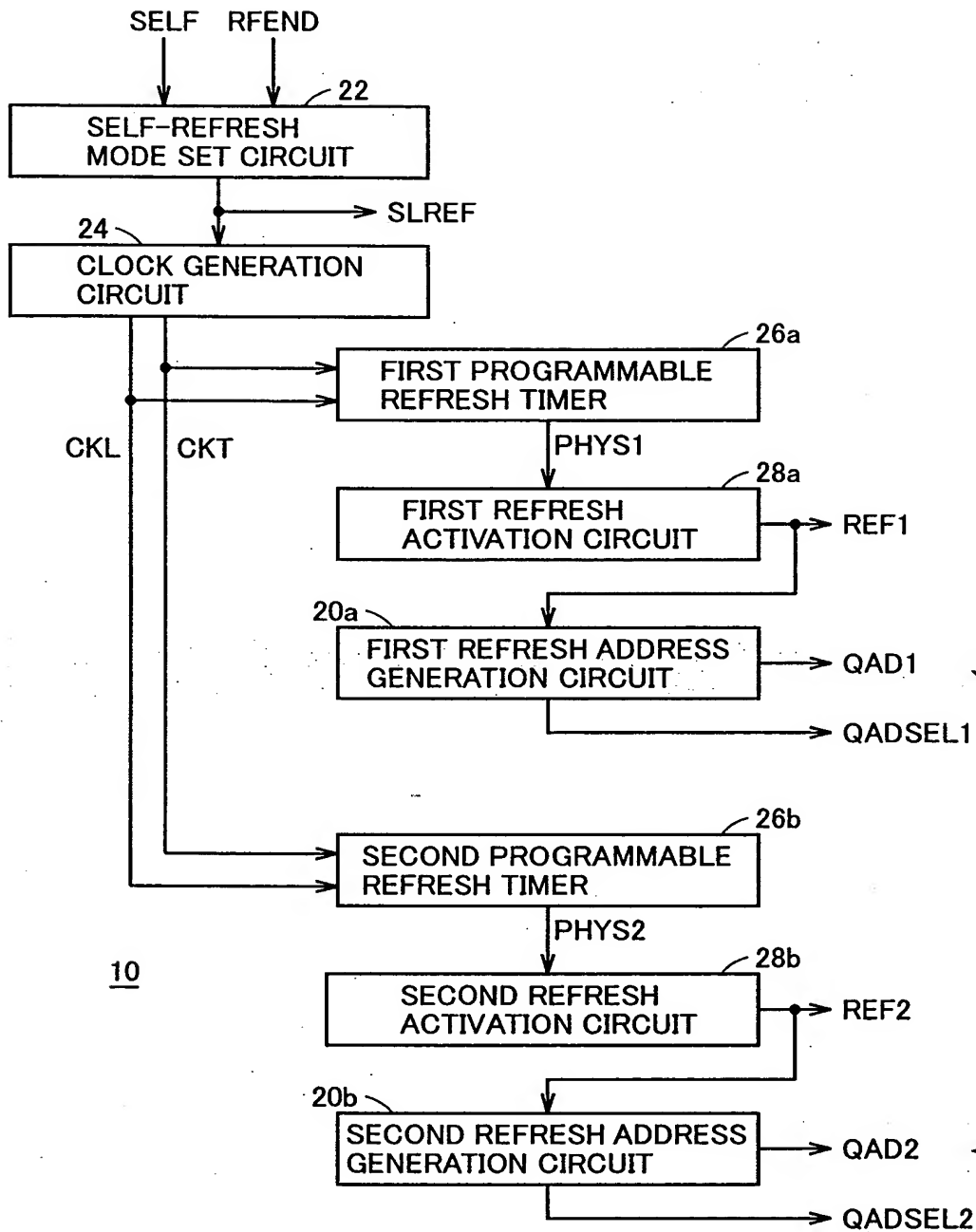


FIG.2



VALIDNESS OR INVALIDNESS IS
PROGRAMMED IN ROW SELECT
CIRCUIT FOR EACH ROW

FIG.3

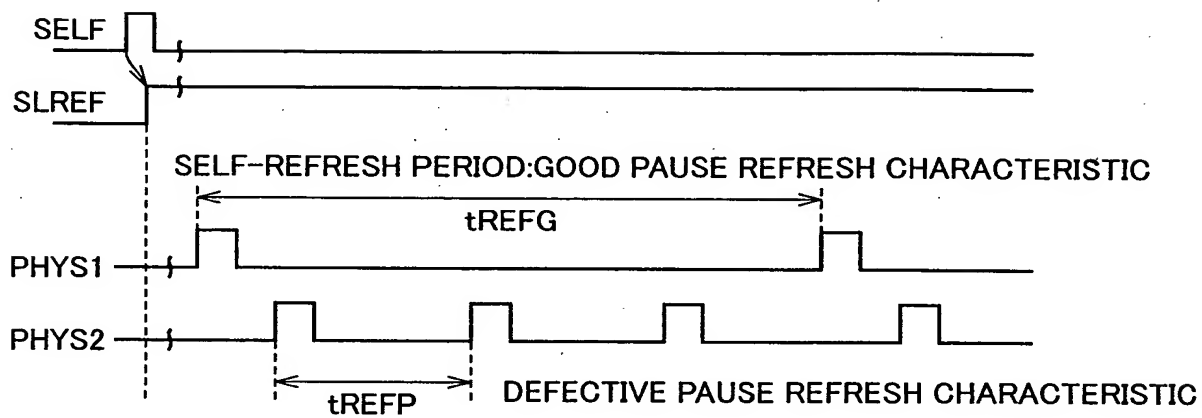


FIG.4

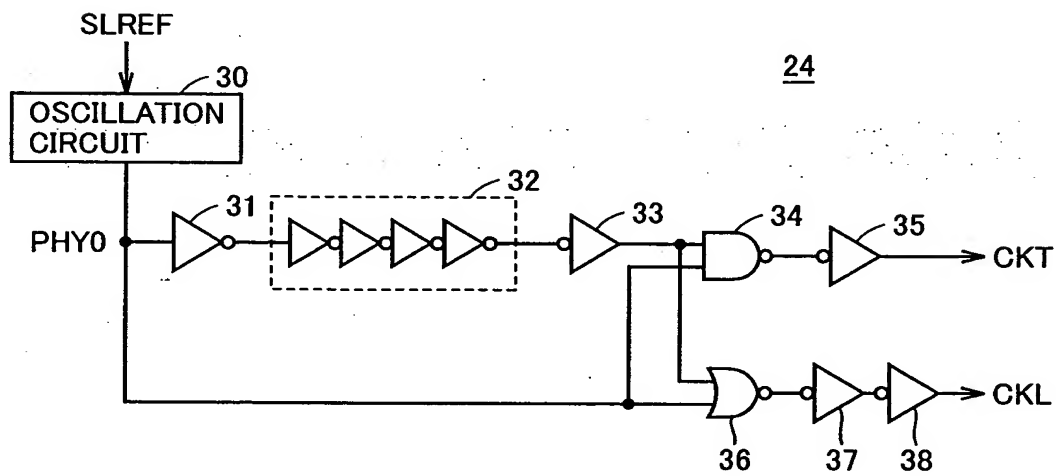


FIG.5

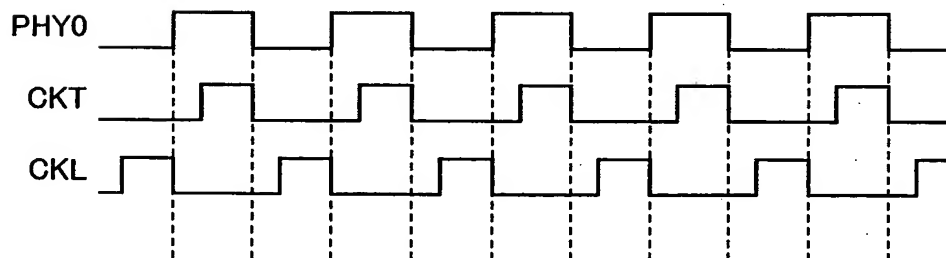


FIG.6

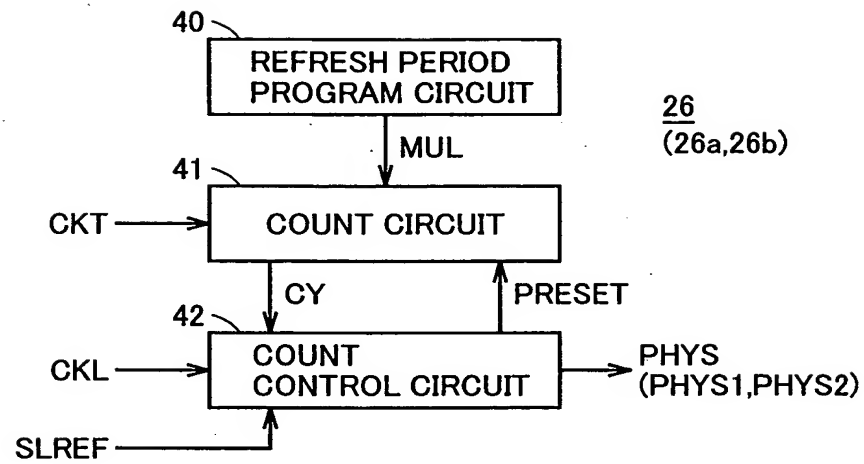


FIG.7

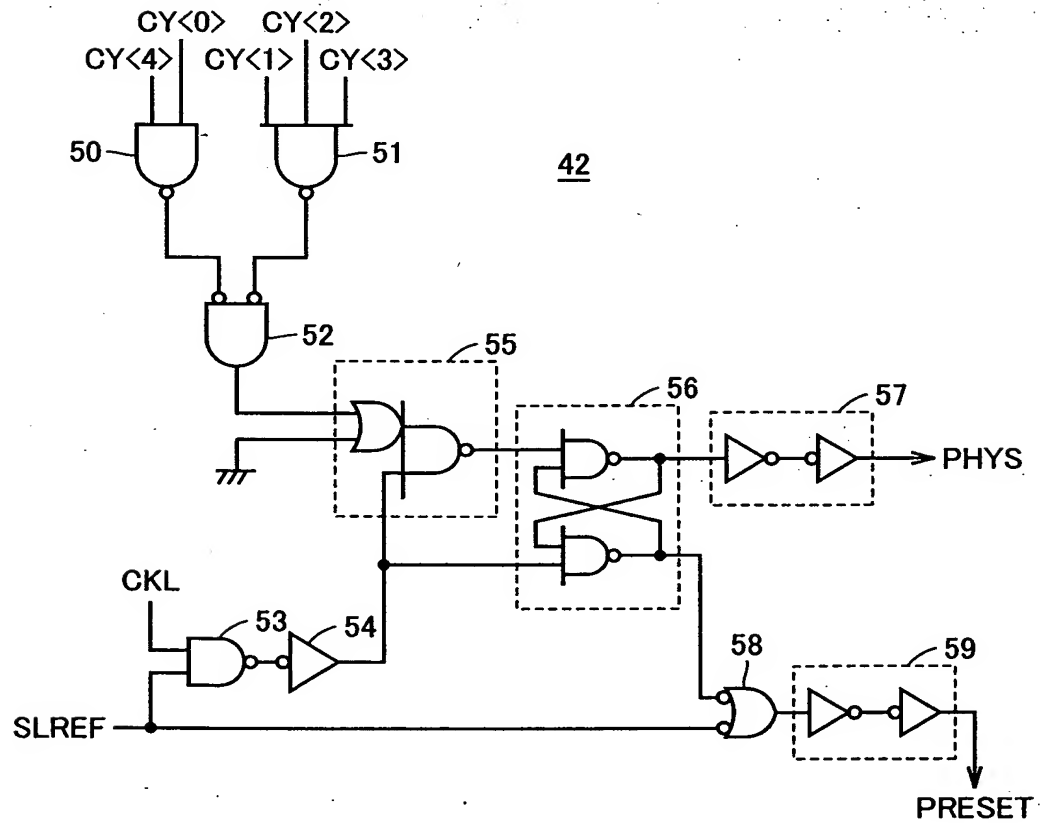


FIG.8

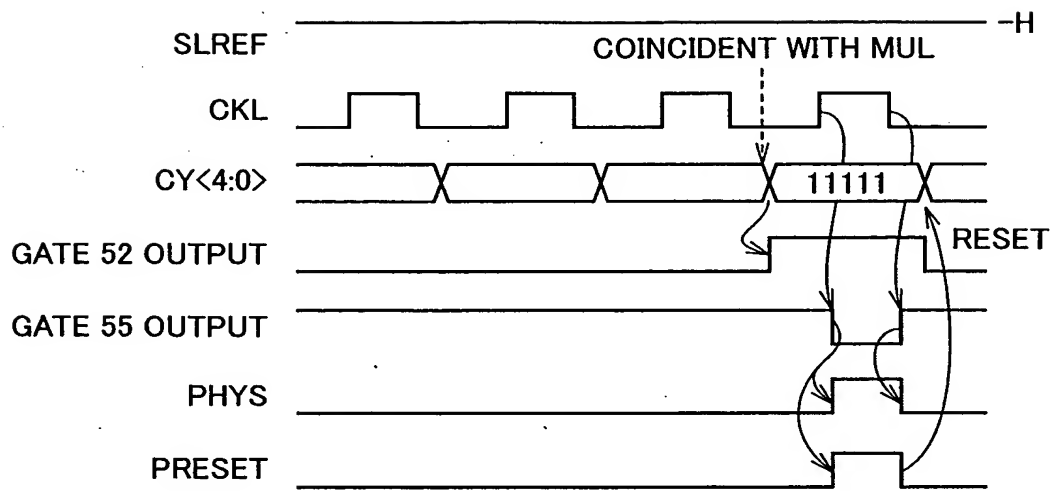


FIG.9

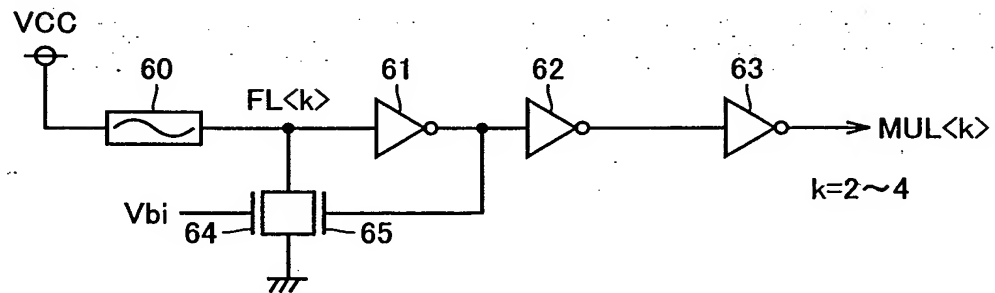


FIG.10

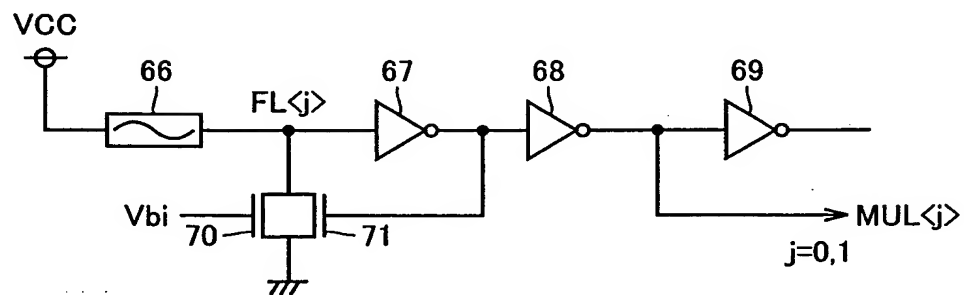


FIG.11

	FUSE STATE	SIGNAL LEVEL	FUSE STATE	SIGNAL LEVEL	FUSE STATE	SIGNAL LEVEL	
MUL<4>	○	L	○	L	×	H	○:NON-BLOWN ×:BLOWN
MUL<3>	○	L	○	L	×	H	
MUL<2>	○	L	×	H	×	H	
MUL<1>	○	H	○	H	○	H	
MUL<0>	○	H	○	H	○	H	

SHORTER ← REFRESH PERIOD → LONGER

FIG.12

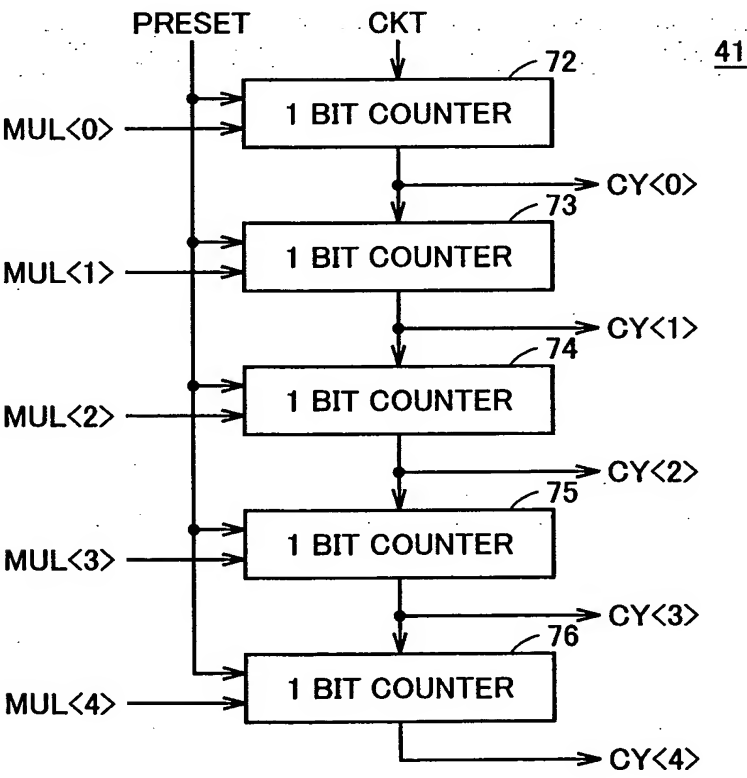


FIG.13

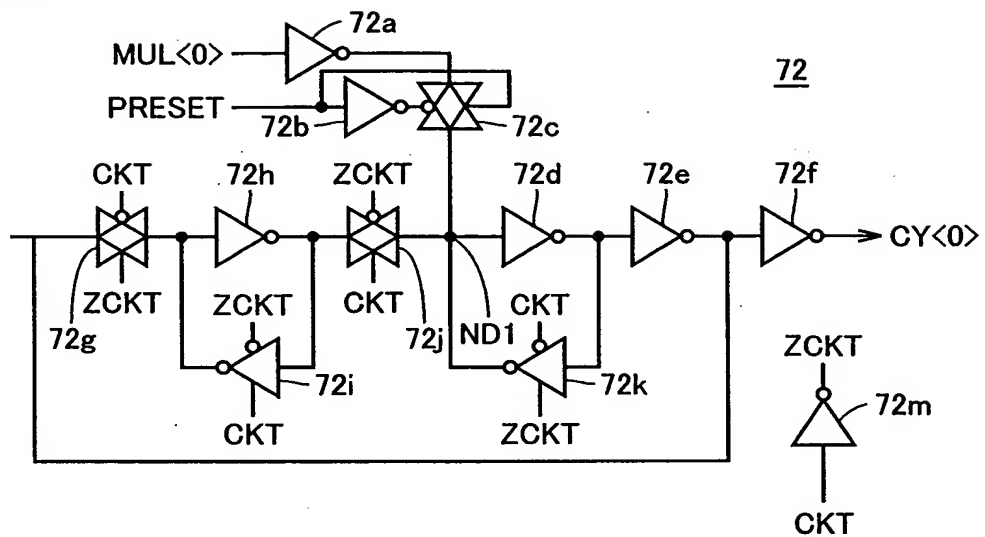


FIG.14

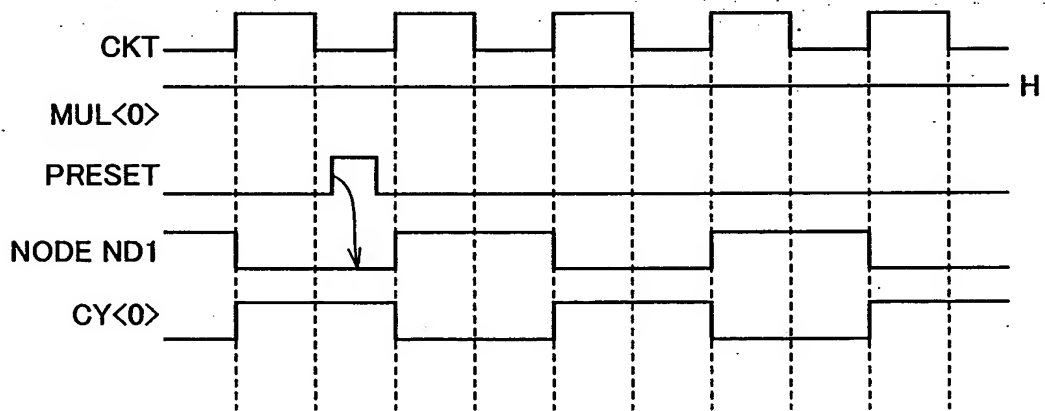


FIG.15

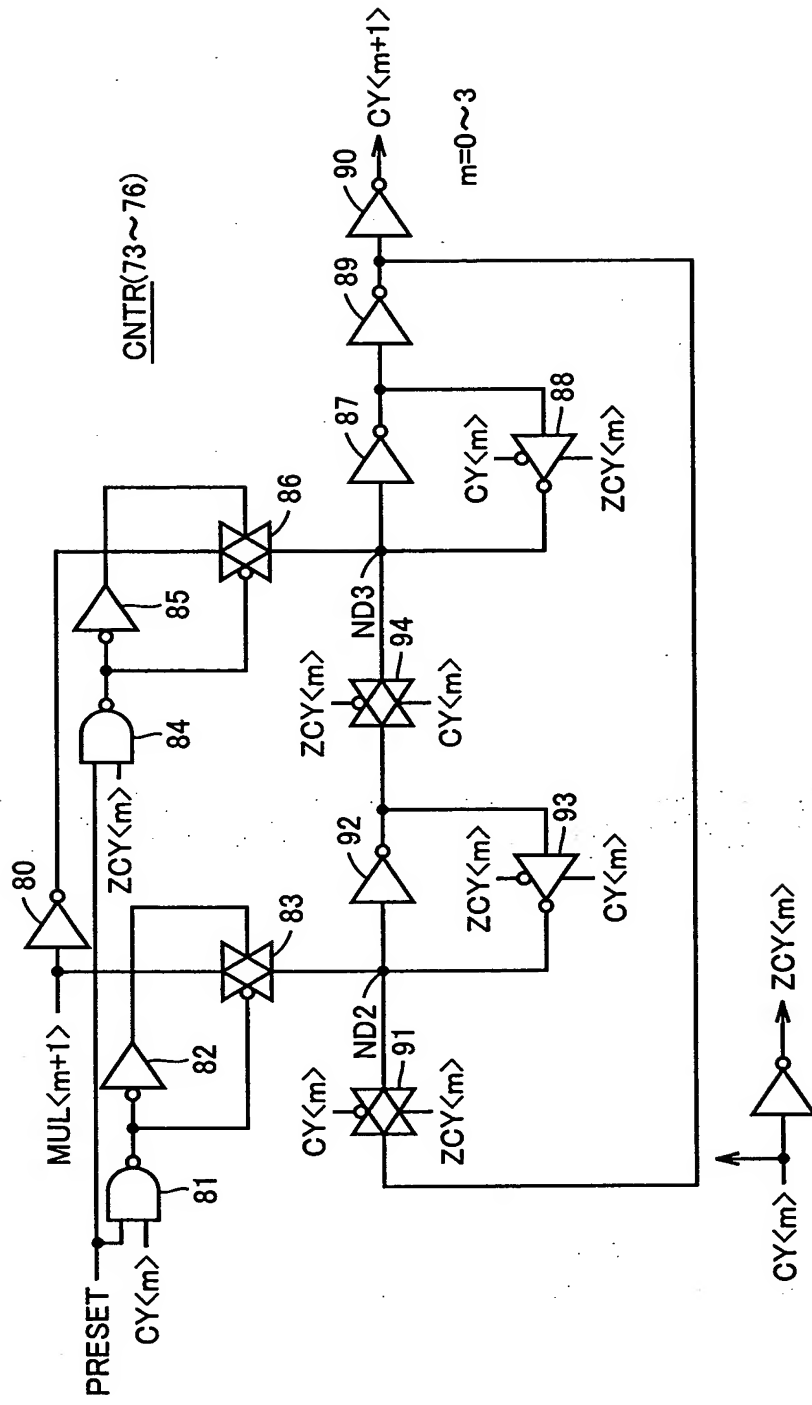


FIG.16

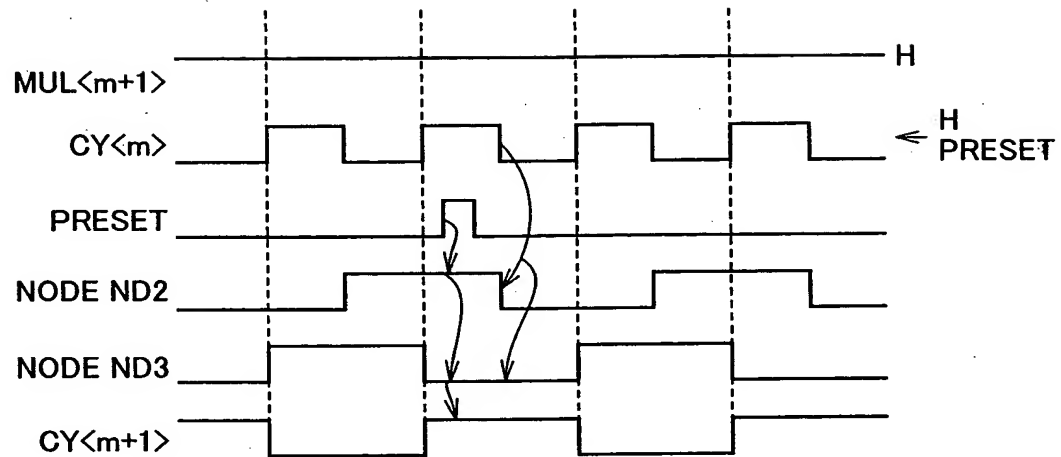


FIG.17

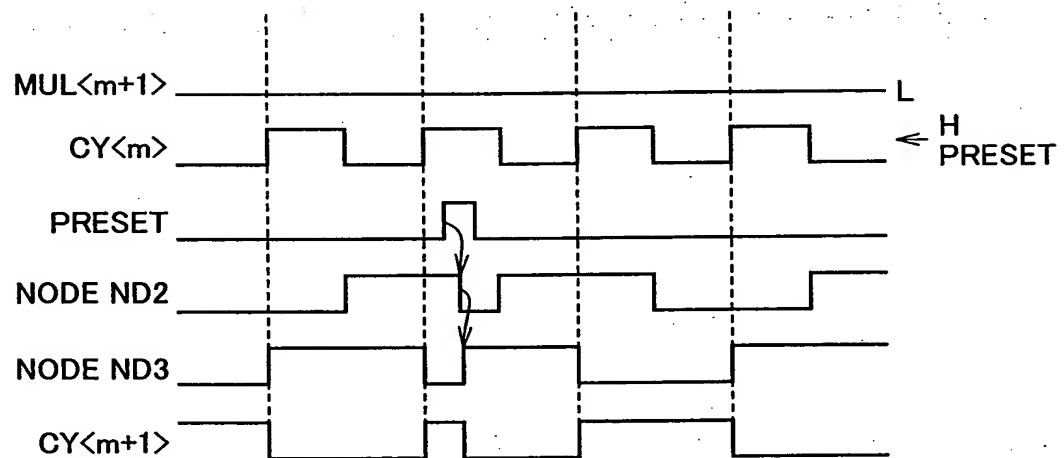


FIG.18

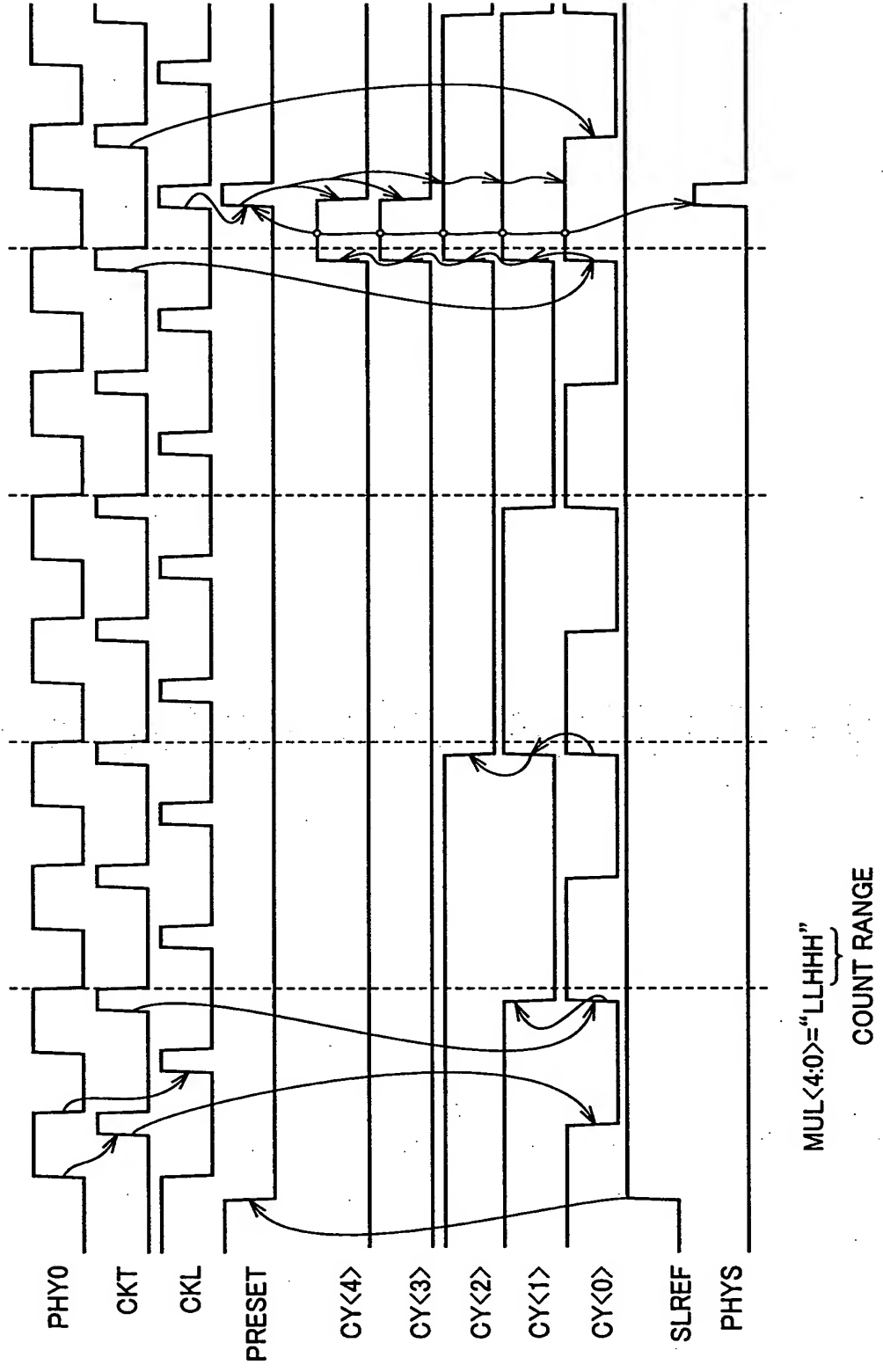


FIG.19

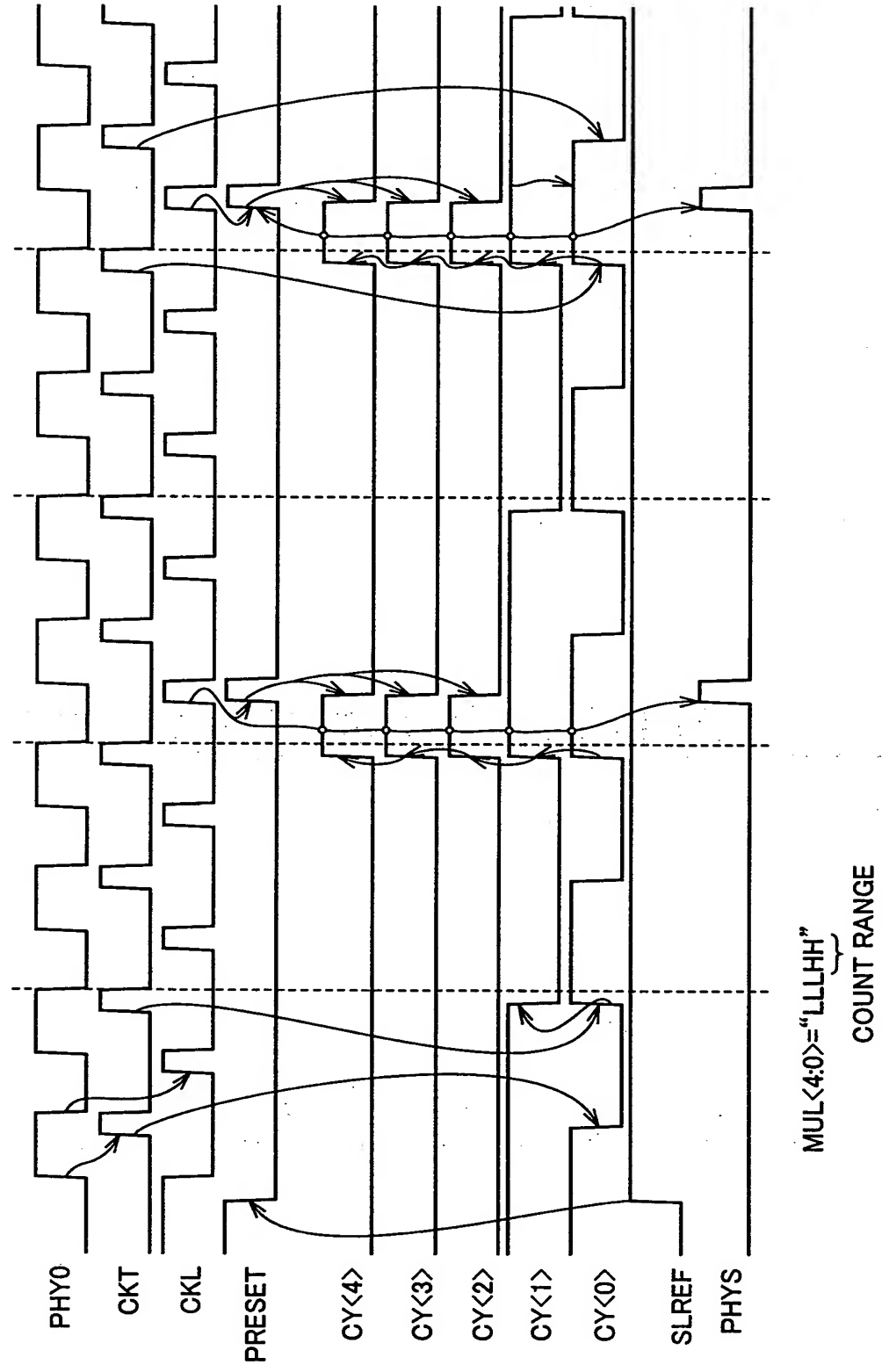


FIG.20

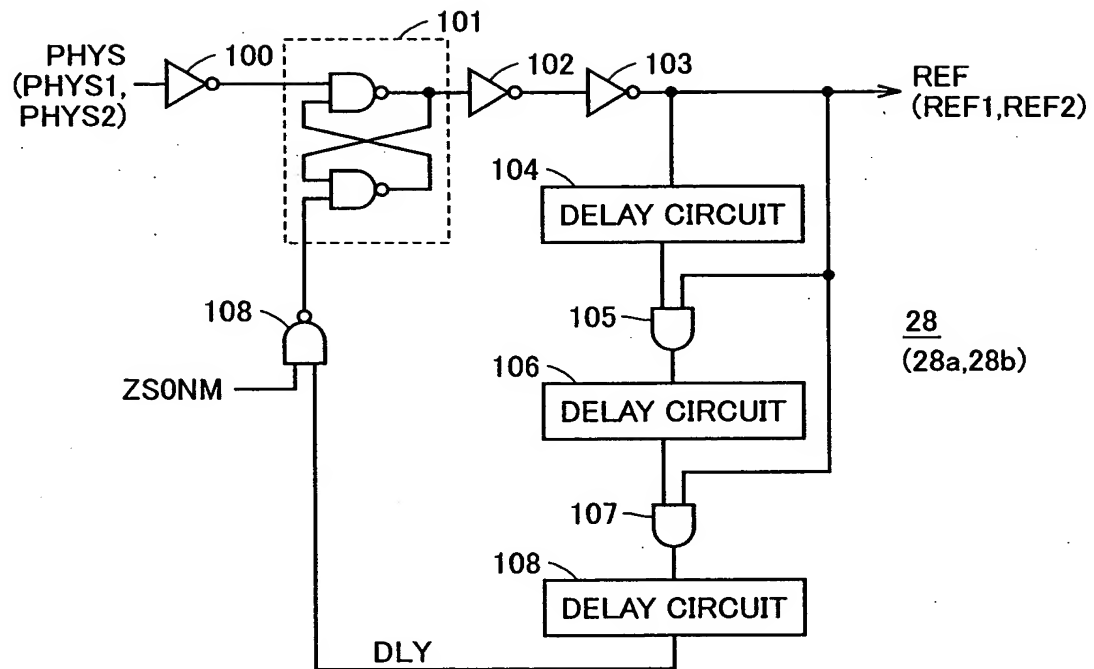


FIG.21

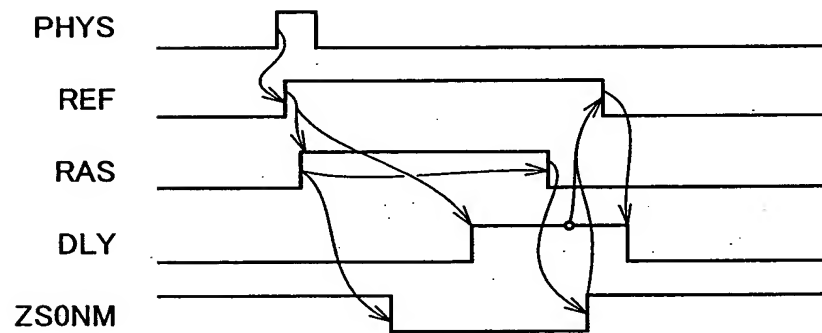


FIG.22

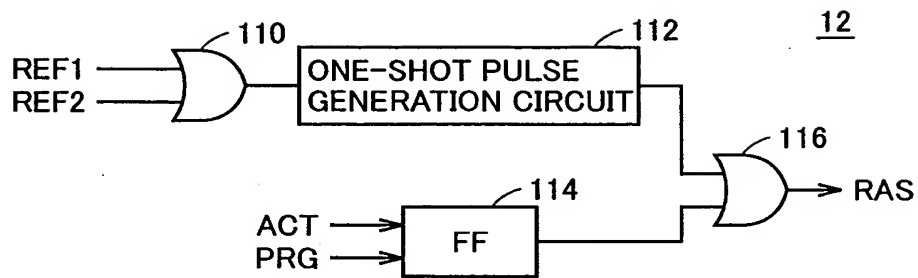


FIG.23

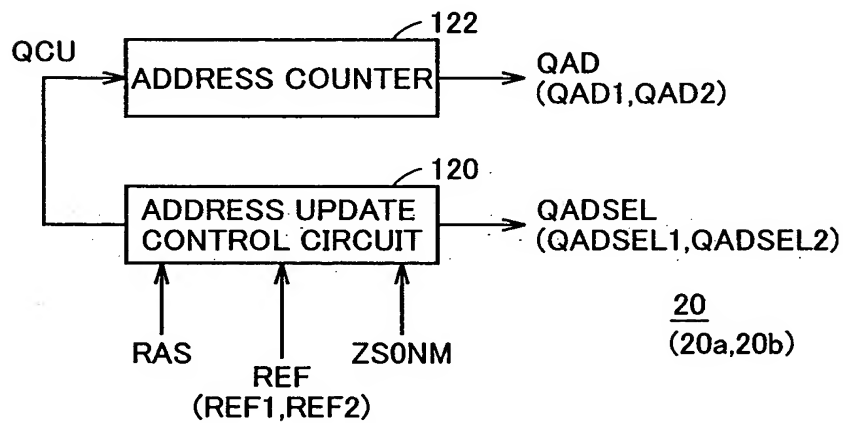


FIG.24

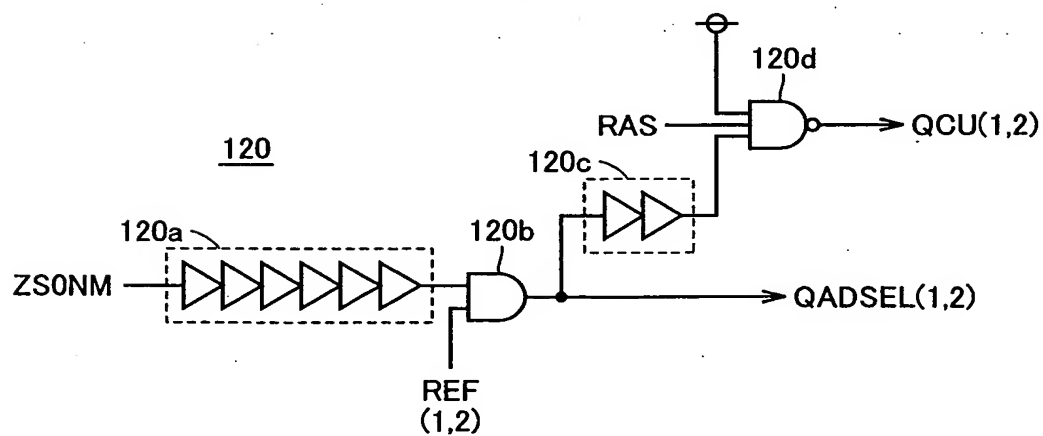


FIG.25

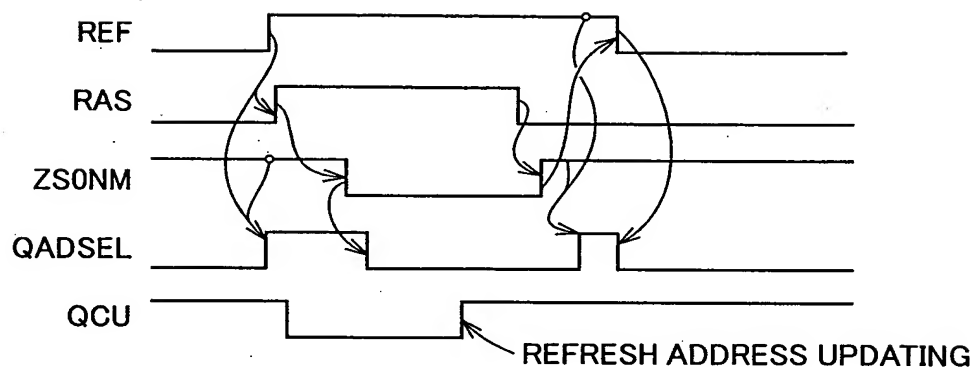


FIG.26

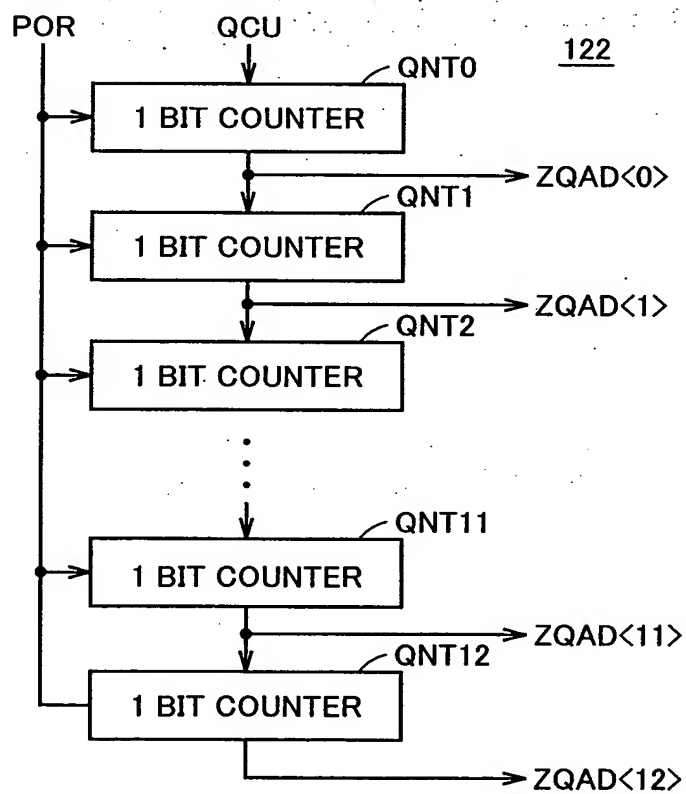


FIG.27

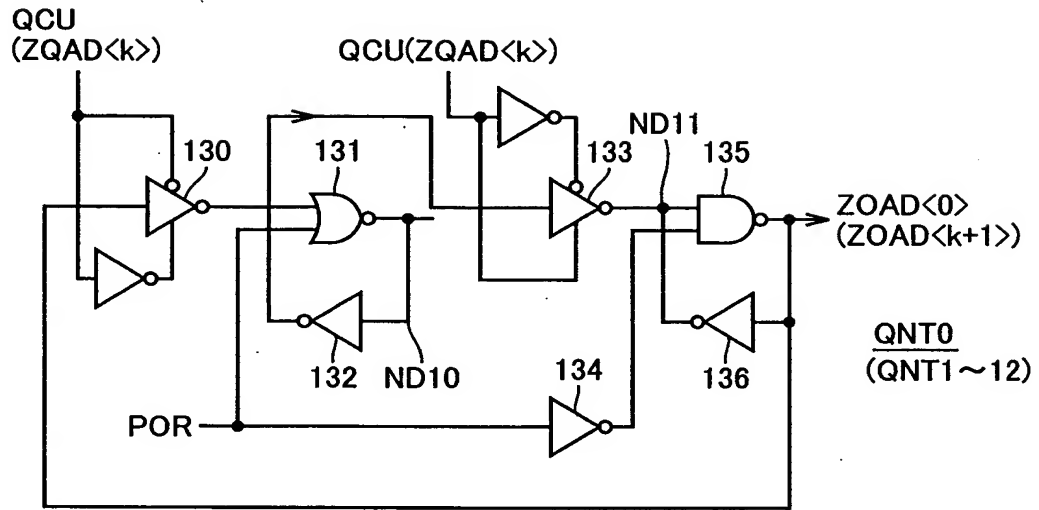


FIG.28

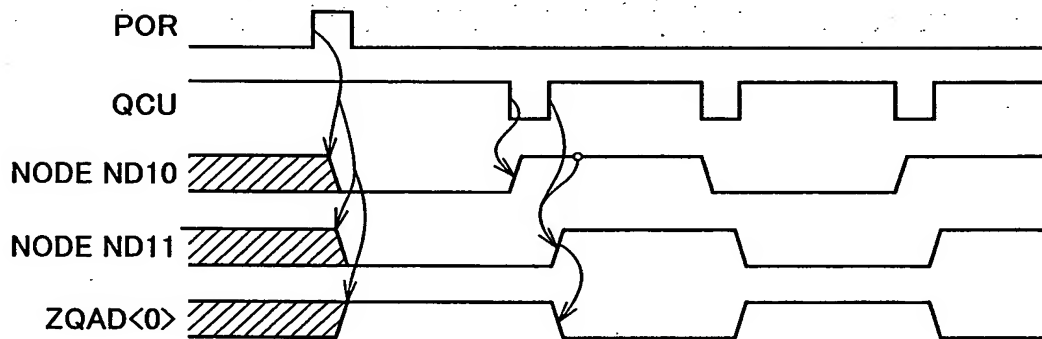


FIG.29



FIG. 30

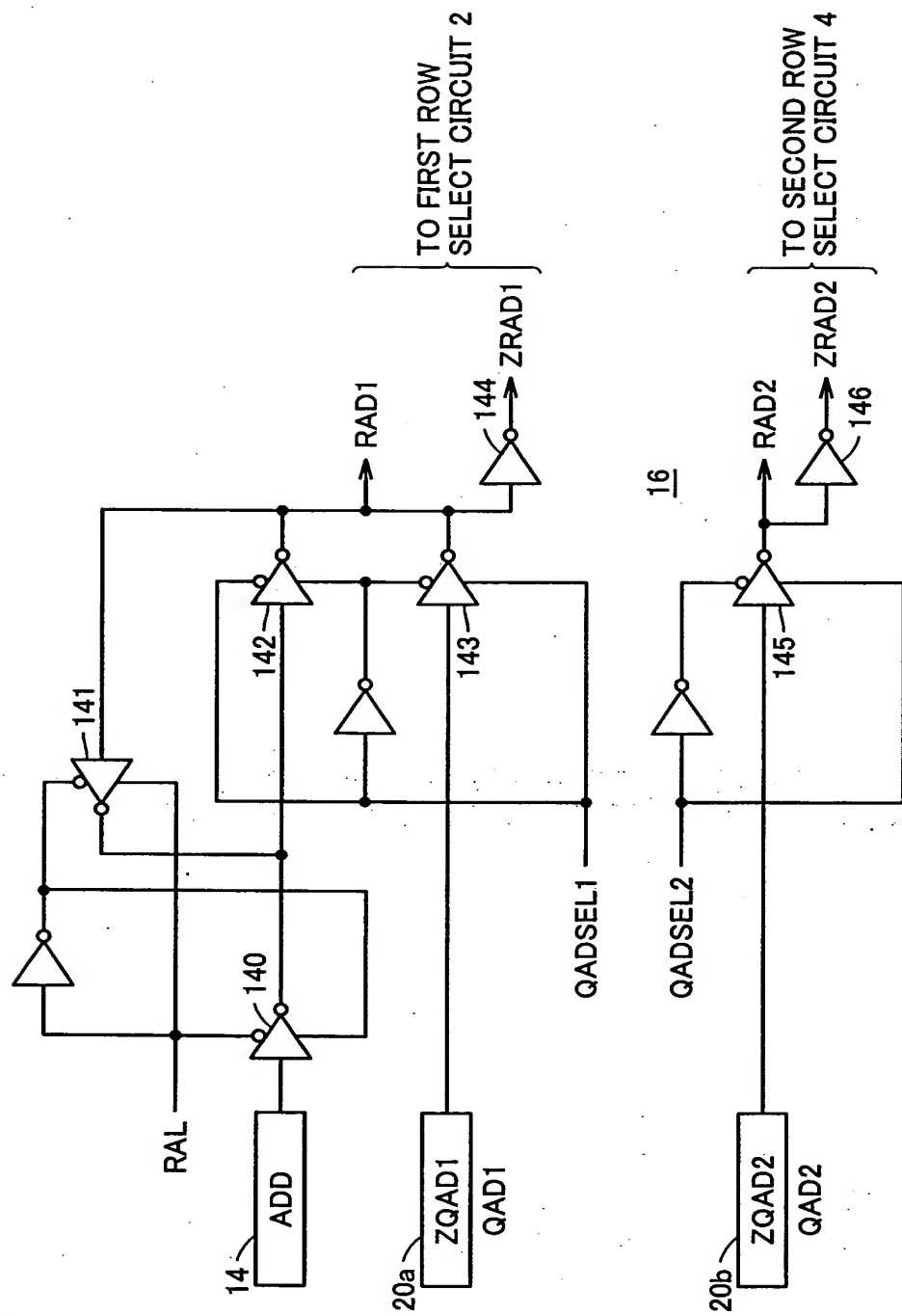


FIG.31

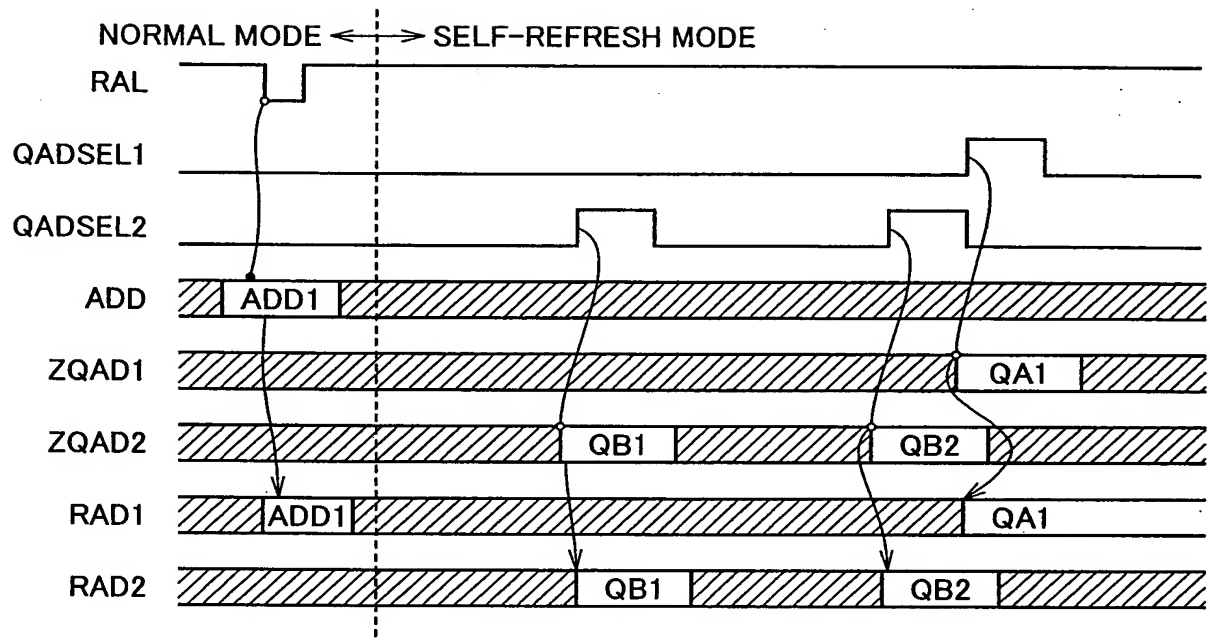


FIG.32

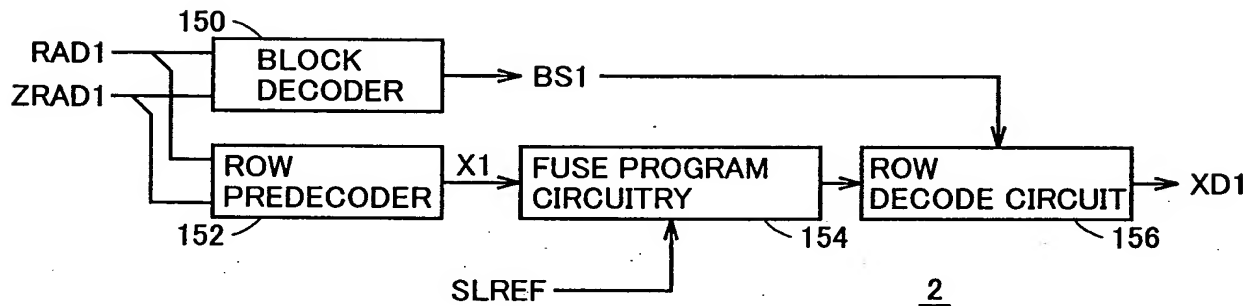


FIG.33

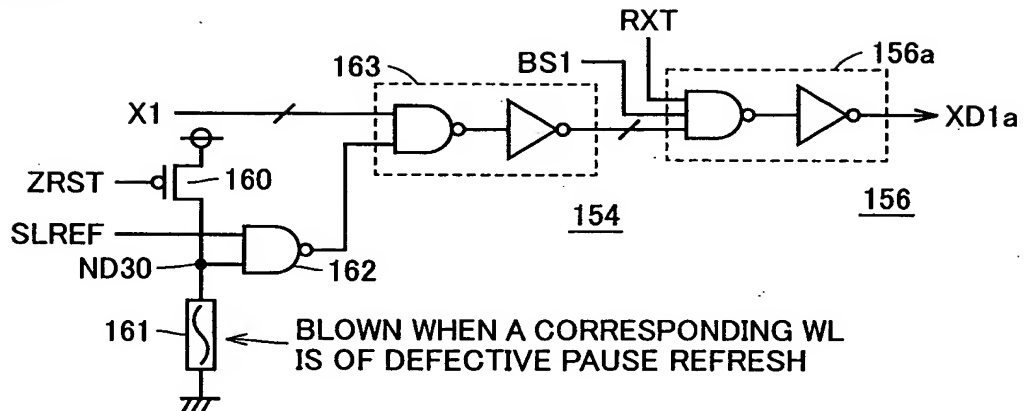


FIG.34

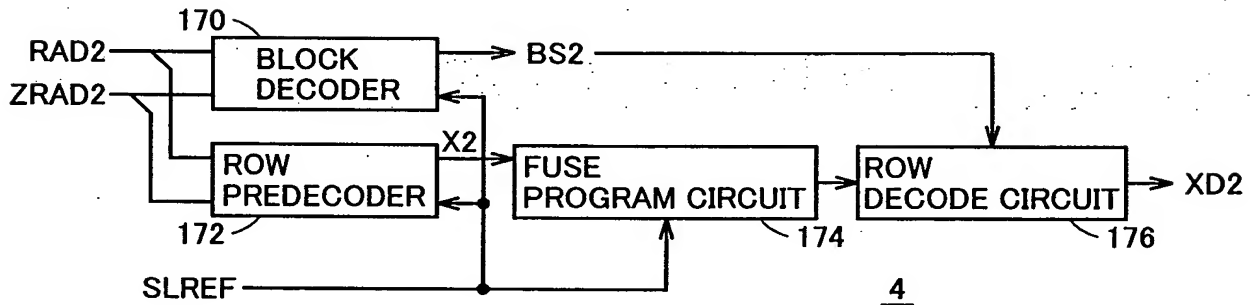


FIG.35

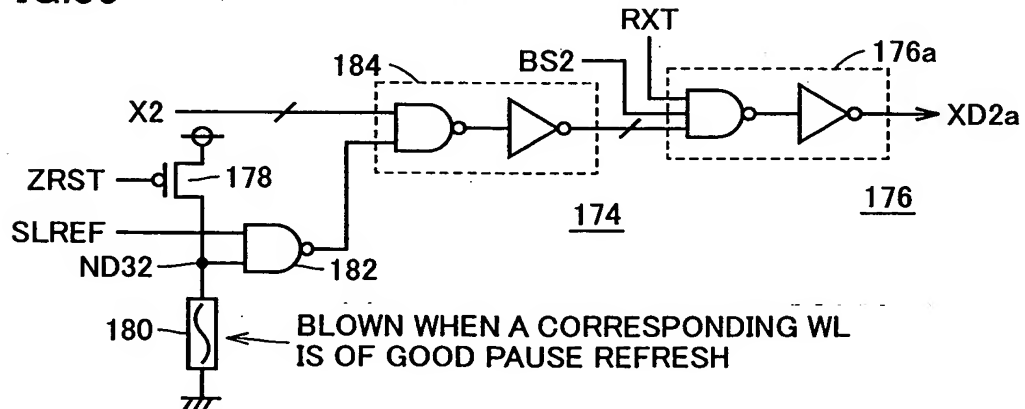


FIG.36

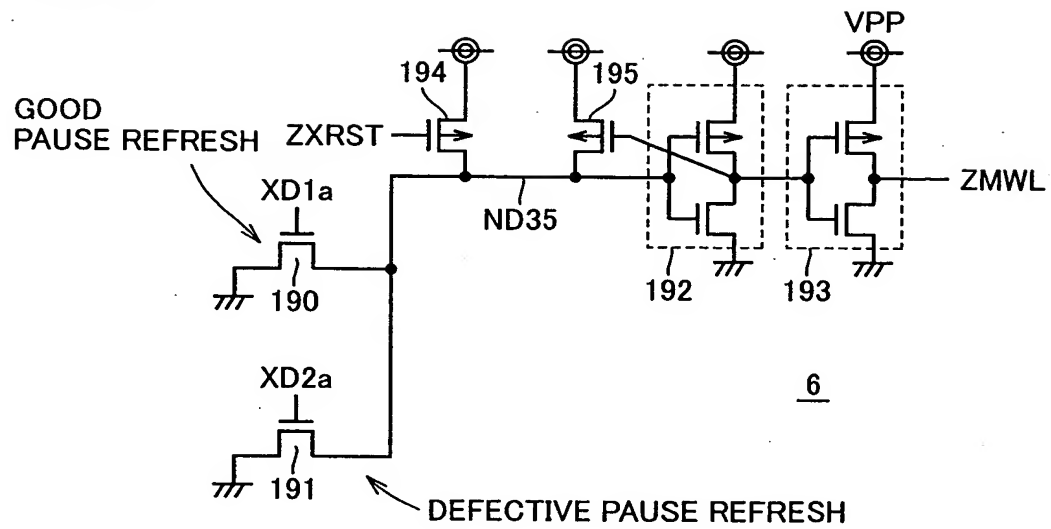


FIG.37

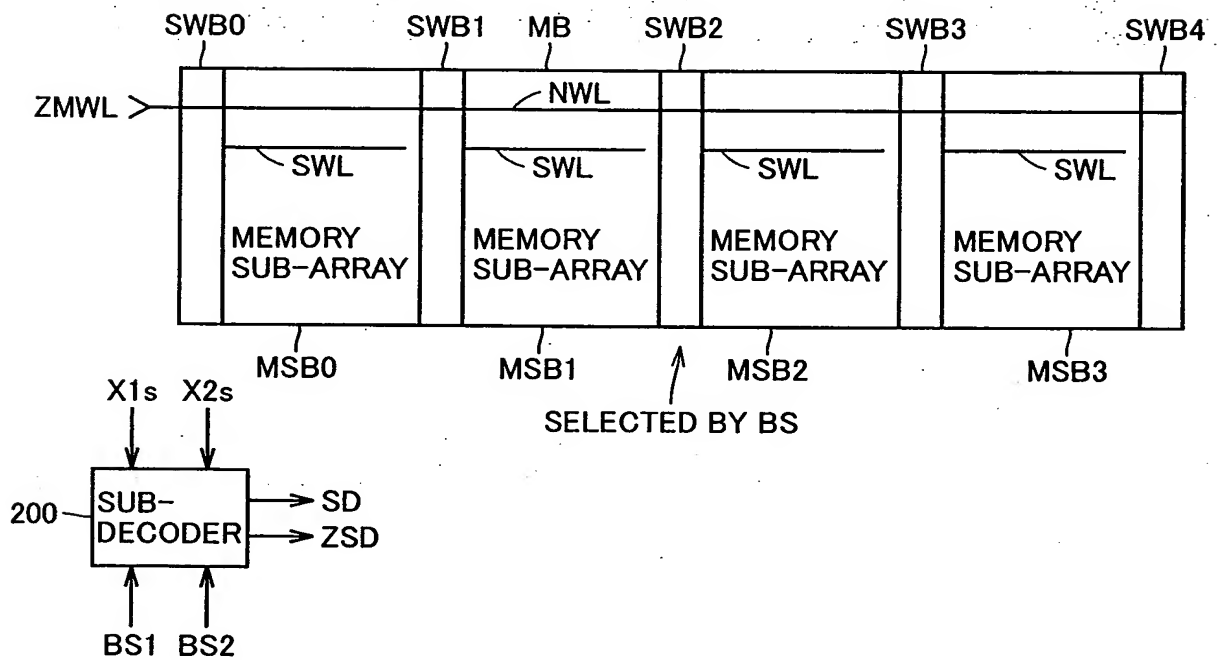


FIG.38

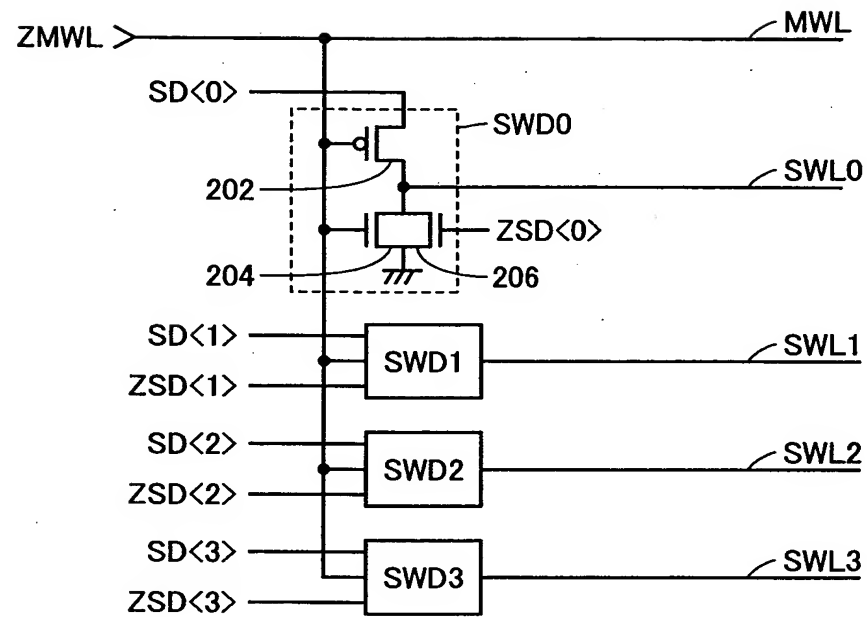


FIG.39

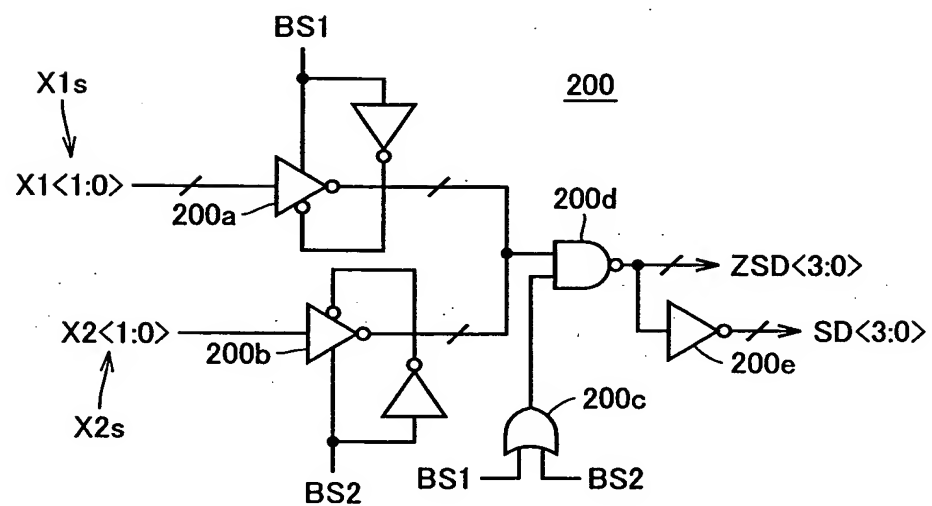


FIG.40

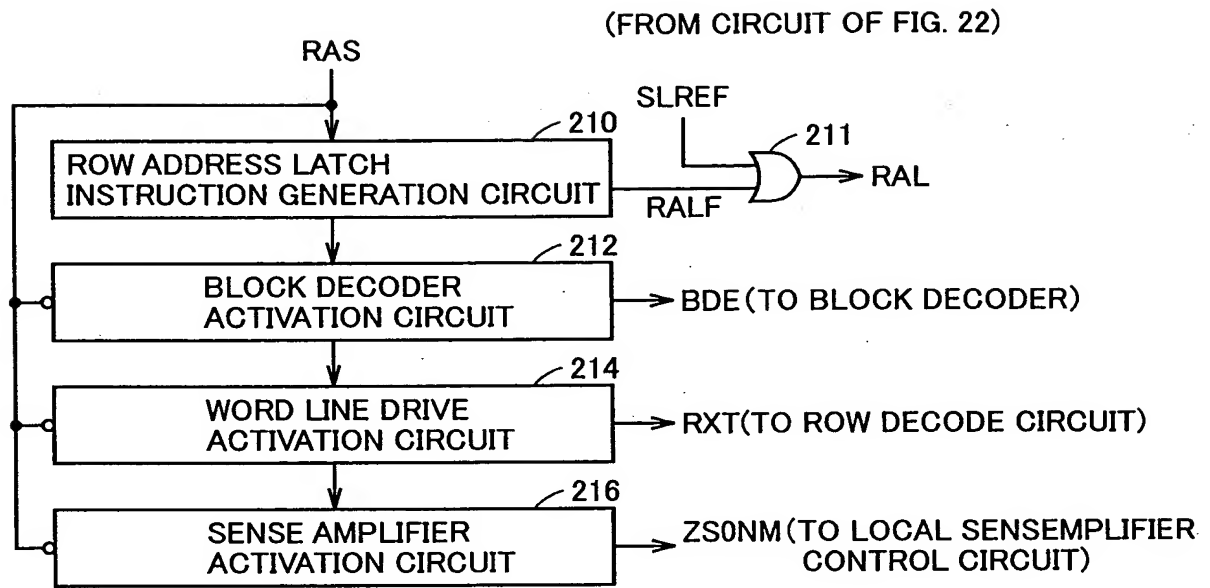


FIG.41

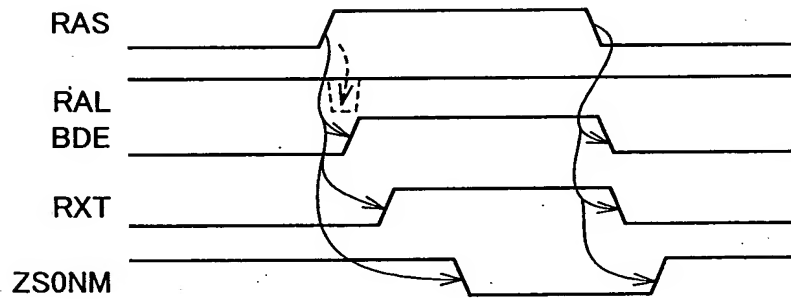


FIG.42

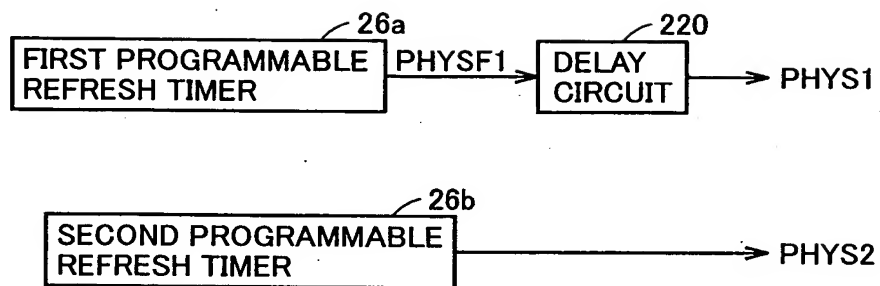


FIG.43

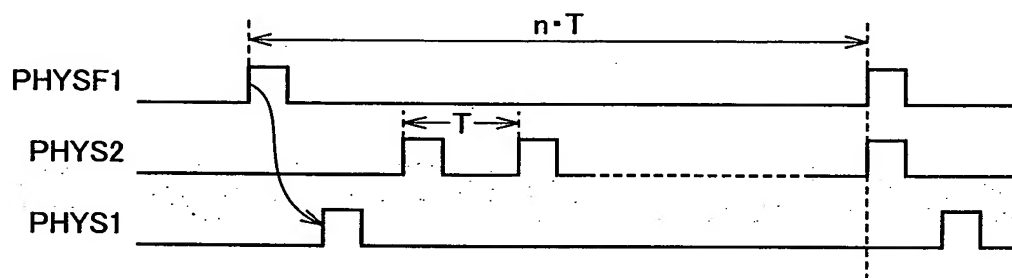
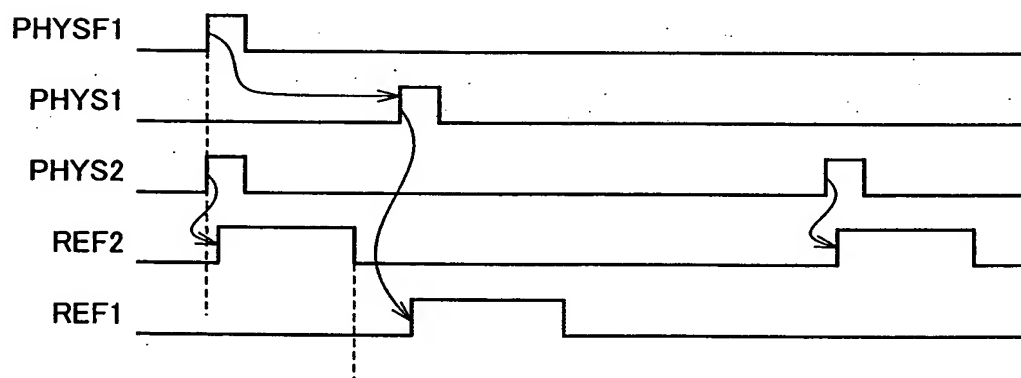


FIG.44



DEFECTIVE PAUSE REFRESH HIT

DEFECTIVE PAUSE REFRESH MISHIT

PHYS2

ZMWL

ZS0NM

ZS0P

S0N

SIGNAL LINE 222